

MP160 data acquisition and analysis systems with AcqKnowledge 5 software provide a flexible tool for life science research.

MP160 SYSTEM SPECIFICATIONS

Analog Inputs

Number of Channels:	16
Absolute Maximum Input:	± 15 V
Operational Input Voltage:	± 10 V
A/D Resolution:	16 Bits
Accuracy (% of FSR):	± 0.003
Input impedance:	1.0 mA

Analog Outputs

Number of Channels:	2
Max output with acquisition:	2 channels
Output Voltage Range:	± 10 V
D/A Resolution:	16 bits
Accuracy (% of FSR):	± 0.003
Output Drive Current:	± 5 mA (max)
Output Impedance:	100 ohm

Digital I/O*

Number of Channels:	16
Voltage Levels:	TTL, CMOS
External Trigger Input:	TTL, CMOS compatible - See also: External Trigger Inputs

*Digital signals accessed with optically isolated STP100C/STP100C-C and STP-IO—separate purchase

Time Base

Min Sample Rate:	2 samples/hour
Trigger Options:	Internal, External or Signal Level

Power

Amplifier Module Isolation:	Provided by the MP unit, isolated clean power
CE Marking:	EC Low Voltage and EMC Directives
Leakage current:	< 8 μ A (Normal), < 400 μ A (Single Fault)

RESEARCH



Device specs	MP160
Max Sample Rate MP Internal Memory:	200 K samples/sec (400 K aggregate)
PC Memory/Disk:	200 K samples/sec (400 K aggregate)
Internal Buffer:	6 M samples
Waveform Output Buffer:	500 K samples
Dimensions:	10 cm x 11 cm x 19 cm

SIGNAL CONDITIONING MODULE COMPATIBILITY

• O ₂ 100C/ CO ₂ 100C	• EDA100C/EDA100D	• EGG100C/EGG100D	• AMI100D
• DA100C	• PPG100C/PPG100D	• EMG100C/EMG100D	• LDF100C
• EBI100C	• RSP100C/RSP100D	• fEMG100D	• MCE100C
• ECG100C/ECG100D	• SKT100C/SKT100D	• EOG100C/EMG100D	• STM100C
• EEG100C/EEG100D	• HLT100C	• ERS100C/ERS100D	• OXY100E